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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TOKUDA, Kazuhiko**

Group Art Unit: 3729

Serial No.: 09/928,441 ✓

Examiner: **CHANG, Rick Kiltae**

Filed: **August 14, 2001**

P.T.O. Confirmation No.: 8352

gwd6/05/03

FOR: **A METHOD OF FORMING WIRING LINES ON A BOARD
TO FORM A CIRCUIT BOARD (AS AMENDED)**

RESPONSE UNDER 37 CFR §1.116
- EXPEDITED RESPONSE -
GROUP ART UNIT 3729

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED
MAY 12 2003
TECHNOLOGY CENTER R3700

May 8, 2003

Sir:

In response to the Office Action dated **February 11, 2003**, please amend the above-identified application as follows:

IN THE CLAIMS: ✓

Claims 1-32 (Canceled)

33. (Original) A method of forming wiring lines on a board to form a printed circuit board, comprising the steps of:

- (a) forming the wiring lines of a predetermined uniform thickness; and
- (b) applying a conductive material on a first wiring line thereof so that the first wiring line has a first portion thicker than a second portion thereof.

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